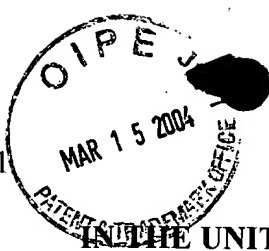


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PATENT

2123

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hakewill, et al.
App. No. : 09/418,663
Filed : October 14, 1999
For : **METHOD AND APPARATUS
FOR MANAGING THE
CONFIGURATION AND
FUNCTIONALITY OF A
SEMICONDUCTOR DESIGN**
Examiner : Eduardo Garcia-Otero
Art Unit : 2123



27299

#19

PATENT TRADEMARK OFFICE

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

March 10, 2004

(Date)

Robert F. Gazdzinski
Reg. No. 39,990

Commissioner for Patents
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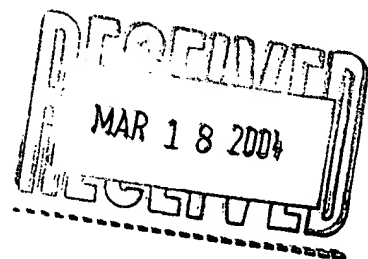
Technology Center 2100

ASSOCIATE POWER OF ATTORNEY

Dear Sir:

The undersigned attorney of record in the above-referenced application hereby appoints the following attorneys and agents of the firm of Hunton & Williams LLP, 1751 Pinnacle Drive, Suite 1700, McLean, Virginia 22102:

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Appl. No. : 09/418,663
Filed : October 14, 1999

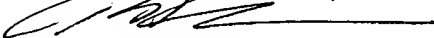
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Scott F. Yarnell, Reg. No. 45,245;

an associate attorney with full powers to prosecute said patent application and make amendments therein and otherwise transact all business in the Patent and Trademark Office in connection therewith.

Respectfully requested,

GAZDZINSKI & ASSOCIATES

Dated: 3/10/04

By: 

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